

UNDERSTANDING

IT Security Administration

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SECO-Institute IT Security Foundation Courseware

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PREFACE

In modern society it is almost impossible to spend a day without using any IT facilities. IT is everywhere and our dependency on it is increasing by the day. This is great, as it brings many benefits, but IT is complex and that means it can fail in many ways. Some of those failures will be caused by defects, others by manipulation. Whichever the cause, it is up to information security professionals to manage the risk of those failures.

An adagio that is often referred to in information security is “people, process, technology”. This reminds us that it takes people with the right knowledge, competences and behaviour, adequate organisational processes, and proper use of technology to establish an environment wherein information can be used and managed in a secure manner.

This book focusses on the technological aspects of information security, i.e. IT security. The human and organisational aspects of information security are the focus of another book (“Understanding Information Security Management”).

The goals of this book

Every highly specialised area of expertise has its own vocabulary to describe the concepts and tools that are most important to it. IT is no exception to this rule. Anyone who has ever worked in or with IT will have quickly realised just how much jargon is used in this field. The number of terms, acronyms, abbreviations, and product names can truly be overwhelming.

Most IT functionaries specialise in one or a few branches of IT, such as network or system administration, but an IT security administrator is involved in all aspects of IT. This means IT security administrators must be able to communicate and work with all branches in IT. To do this effectively they need to master the language used by all those branches. One of the main goals of this book, therefore, is to familiarise the reader with the vocabulary used in IT security administration.

IT security administrators are responsible for designing and maintaining an environment that offers the level of security needed by their organisation. To do that they must understand what tools are available, and how they can be used to increase the security of the environment as a whole. However, in most organisations IT security administrators are not responsible for actually implementing the solutions used to create a secure environment. That most often is entrusted to network, system, database, or application administrators, which makes it necessary for an IT security administrator to understand

their way of working. Thus, another main goal of this book is to familiarise the reader on a conceptual level¹ with the functionality provided by the most common tools and protocols seen in a secure environment.

Finally, IT security administrators have their own field of expertise, which is not always familiar to other IT administrators. To make sure the environment for which they are responsible is and stays secure IT security administrators need to know about vulnerabilities, threats, and attacks. They need to understand how malicious agents operate, and what can be done to prevent malicious agents from compromising the security of networks, systems or data. Introducing the reader to how attackers and defenders operate is the final goal of this book.

This book assumes the reader has no prior knowledge of IT security, but does aspire to gain the knowledge and understanding necessary to become an IT security administrator. Although this book requires no prior knowledge, affinity with technology will definitely make it easier for the reader to grasp all the concepts introduced in this book. IT security administration is an extremely broad topic, which means this book has to cover a lot of ground. Therefore some perseverance might be needed too.

The structure of this book

In IT, people use systems to accomplish their tasks. These systems are comprised of hardware and software. Hardware, such as displays, keyboards, storage devices, network cards, and peripherals, acts like an interface between the physical world and the digital world. It enables systems to perform their tasks and deliver the results to a user or another system. Software is what makes hardware intelligent and flexible. It enables a system to execute instructions and process data that were not built in the system during manufacturing. General-purpose software can be divided into three categories: Operating Systems that make a system useful for many purposes, applications that can be used to execute specific tasks, and services that can be used to offer remote functionality to a broader public than just the people that can physically access the system. To make communication between systems possible a network is needed. And to make sure all traffic is efficiently handled the network makes use of special devices, like switches and routers. Local networks can offer services to their constituency, but can also be linked to each other to create larger networks, which together make up the Internet.

¹ To make the contents of this book applicable to as wide a range of environments as possible it will limit itself to concepts and will not go into the specifics of particular products.

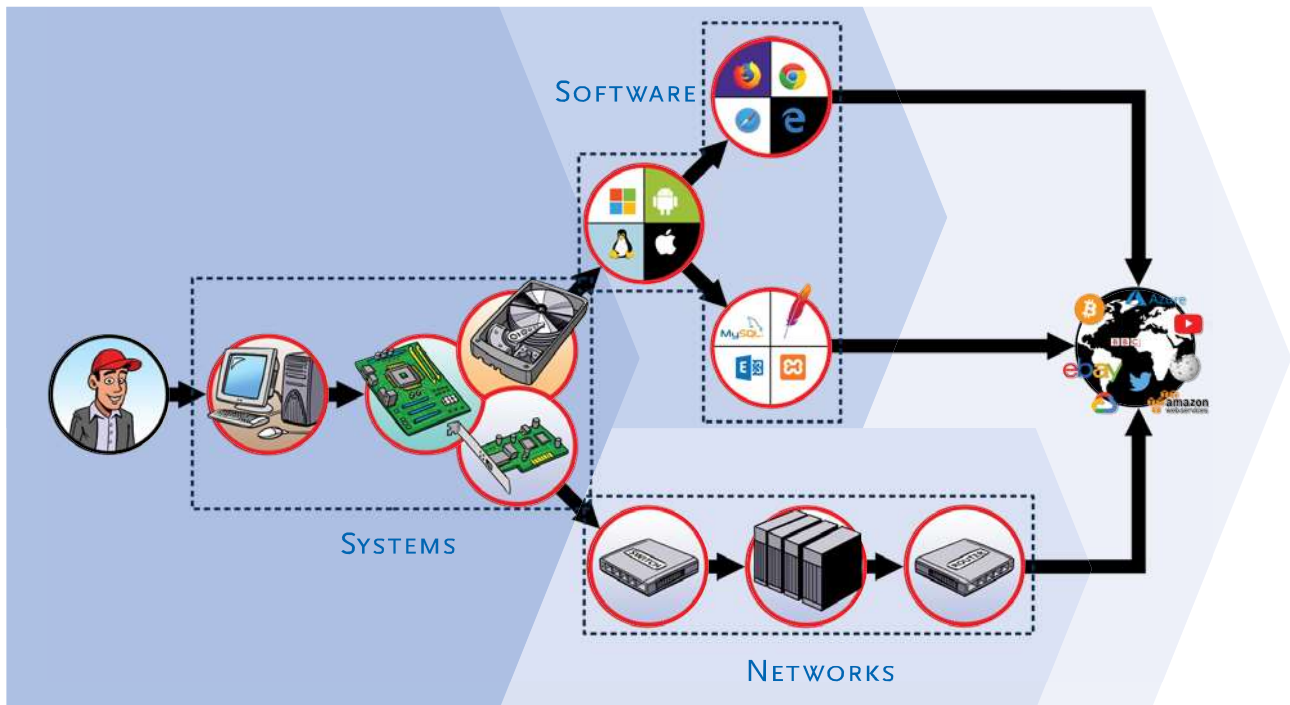


Figure 1: The scope of this course on IT security administration

This book follows the logical structure described above.

Domain 1 “Systems” describes the most important hardware components that are found in systems and explains how they function (Chapter 1). It also explains what an Operating System is and how it manages the hardware of the system it is running on (Chapter 2). This domain ends with an overview of how systems can cooperate with each other (Chapter 3).

Domain 2 “Software” presents the main types of software (Chapter 4). It analyses how software processes instructions and data, and describes the types of errors that can occur during processing (Chapter 5). Understanding this domain does not require any programming knowledge; the topics are discussed on a conceptual level, focusing on the causes and potential impact of software errors. Sufficient detail is provided to understand the security bulletins published by manufacturers of software, security researchers, and other parties. In essence databases are software, just like any other application or service, but because they are so important to processing and storing data, and because their security involves specific mechanisms they are covered in a separate chapter at the end of Domain 2 (Chapter 6).

Domain 3 “Networks” takes a look at the devices that are used to build a network and manage the traffic that is sent over it (Chapter 7). It also describes the most common types of connections between network components (Chapter 8). To understand what is involved in making networks the versatile and useful tools they are, the two most important network models are described (Chapter 9). Building on this theoretical knowledge the fundamentals of designing secure networks are covered (Chapter 10). The domain is closed with a description of how individual nodes within a network can be accessed (Chapter 11).

Domain 4 “Cryptography” explains the fundamentals of cryptography, with a focus on public key encryption (Chapter 12). This is followed by an overview of how Public Key Infrastructure can be used to manage encryption and encryption keys in an enterprise environment (Chapter 13). Domain 4 is concluded with an overview of the most important cryptographic networking protocols (Chapter 14) and applications (Chapter 15).

Domain 5 “IT security administration” brings together all the information covered in the previous domains. It shows how attackers find vulnerabilities in IT infrastructures and how they exploit them to gain unauthorised access to data and systems (Chapter 16). Subsequently, it outlines how an IT security administrator can secure IT infrastructures and minimise an attacker’s chances of success. It also outlines what to do when an attack succeeds in spite of all security efforts (Chapter 17).

The annexes provide additional practical information on some of the topics covered in this book.

Notes to the reader

Some of the exercises included at the end of each domain use practical examples to illustrate how the knowledge covered in this book is applied in practice. These examples are situated in a fictitious company, called Bicsma. This organisation was created by the SECO Institute to provide context for the assignments and case studies used in SECO’s educational material.

Bicsma is a Dutch beverage manufacturer. The company is managed by three brothers: Vincent, Hans, and Marcel Bicsma.

Bicsma was launched in the family’s kitchen when Ma Bicsma, the family matriarch, started producing home-made fruit juices for local supermarkets. The juices were so well-received that Bicsma soon became a medium-sized company with two production facilities and 354 employees.

Bicsma is engaged in both business-to-business and business-to-consumer activities. In a somewhat unusual manner, the company sells drinks to both supermarkets and individuals.

The summaries and test questions included in this book help readers to prepare for the SECO Institute's IT Security Foundation certification exam. The test questions reflect the content and difficulty of the certification exam, but do not fully mimic the format used in the actual exam, which consist of multiple choice questions only. The open questions included in this book are intended to encourage creative and critical thinking. These questions do not always have an answer that is 100% correct or wrong. They are aimed at helping you find your own best approach to a problem.



The background is a solid blue color with several white, semi-transparent geometric elements. In the upper left, there are several thin, parallel white lines that appear to be part of a larger structure. A large, white, irregular polygonal shape is positioned on the left side, partially overlapping the blue background. To the right of this shape, there is a white, L-shaped or bracket-like structure. The overall composition is modern and minimalist.

DOMAINS



DOMAIN 1: SYSTEMS

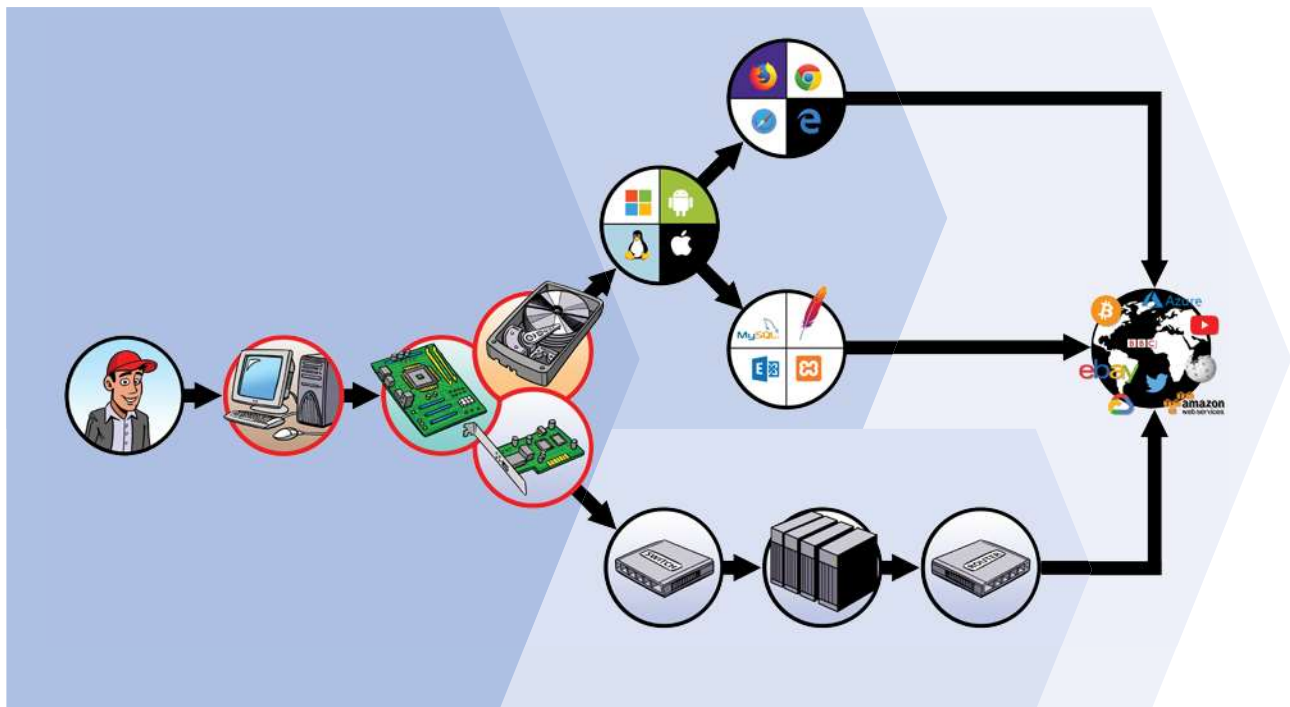


Figure 2: Systems in the scope of IT security administration

INTRODUCTION

Everything in the digital domain runs on some type of system. For that reason alone every IT security administrator needs to cooperate with system administrators. To be an effective security administrator you must understand what they do and know at least some of the vocabulary they use. You need to be able to communicate with system administrators about threats and work with them to design effective security measures that mitigate (some of) the risks those threats pose to your organisation.

The core of each system is some form of computing hardware combined with an Operating System (OS) that provides a uniform interface to the users of that hardware. This core often is complemented with peripherals and application software that enables the system to perform specific tasks.

UNDERSTANDING IT SECURITY ADMINISTRATION

To identify and assess possible threats to a system you need to understand on a conceptual level how each of these four components (hardware, OS, peripherals, and applications) functions and how they interact with each other. In this domain we will take a look at hardware and Operating Systems. Further on in this book we will dedicate a whole domain to software in general.

1 COMPUTING HARDWARE

Computing devices come in a great variety of forms. First you had large systems, like mainframes, with terminals to access them. Then the PC revolution followed, which resulted in the availability of desktop computers and laptops² everywhere. With the rise of the Internet, and later the Cloud, the role of servers that offer all kinds of network services increased again. And most recently mobile devices like tablets, smartphones, and wearables have added a new layer of complexity to the ecosystem of computing devices.

In essence all these devices do not differ that much. They all are made up of an ensemble of computing hardware, an OS, data storage facilities, and interfaces to enable communication (input and output). Of course, the specifics can differ immensely for each type of device, but such differences are not relevant for this chapter.

1.1 Computer architecture

A computer is a highly complex apparatus with a great many separate components that seamlessly work together. To make this possible a clear set of rules has to be agreed upon and implemented. Among other things these rules describe the device's functionality and interoperability, including both physical properties (e.g. voltages, sizes, number and type of connections) and logical properties (e.g. what operations are available, what instructions can be given, and how input and output signals should be interpreted). Collectively these rules are referred to as the **architecture** of the device.

1.2 Central Processing Unit

The heart of each computing device is the **Central Processing Unit (CPU)**. The CPU is what makes a device "intelligent". It provides for the device's most important computing capabilities. A CPU consists of an immense number of transistors organised in integrated circuits (IC) on a chip. Each CPU is built up from three core components: an **Arithmetic Logic Unit (ALU)**, **registers**, and a **Controller**.

The ALU can execute a predefined set of operations that are hardwired into its design. Such operations are either mathematical calculations (e.g. addition and subtraction), logical operations (AND, OR, XOR, or bit inversion), or bit-shifting operations (left shift, right shift, rotation, and a few variations on those). The more complex the operations an ALU can perform, the more impact this has on its overall speed. Because

² Laptops are of course mobile devices, but in terms of functionality and use cases they resemble desktop PCs more than they do tablets or smartphones.

of this ALUs are designed to execute only a limited set of well-defined operations. Currently there are two schools of instruction set design: **Reduced Instruction Set Computers (RISC)**, where the CPU contains a small set of general purpose instructions, and **Complex Instruction Set Computers (CISC)**, where the instruction set includes more complex and specialised operations. Well-known examples of CISC and RISC architectures are the x86 and ARM family of chips, respectively.

As described above the ALU can execute operations, but that's all it can do. So, there needs to be another component that feeds the ALU with a proper sequence of instructions and the data on which to execute them. That is what the Controller does: it is the housekeeper of the CPU. The Controller makes sure that all instructions are executed in the right order, that the data on which to execute the instructions is given to the ALU at the right moment, and that the results are retrieved from the ALU before they are overwritten again. The Controller also translates the software instructions it receives to a sequence of internal hardware instructions to be executed by the ALU. Finally, it regulates when software is given processing time on the CPU.

The third core component of all CPUs are its registers. These are small storage spaces with a fixed location within the CPU that can be read from or written to extremely quickly. Registers can contain instructions or data. To further increase performance some registers are assigned specialised tasks, for example:

- The instruction register, which holds the instruction that is currently being executed.
- Address registers, that can be used to quickly look up a specific address.
- Status registers, that contain information on the current state of the CPU.
- Constant registers, that contain fixed values that are often used, such as pi.

More advanced CPUs often contain additional components, like fast memory caches that increase the CPU's performance by lowering the time it needs to access data. Another important component is the **Memory Management Unit (MMU)** that provides an abstraction layer between the logical memory addresses used by software and the physical addresses used by hardware.

Some examples of well-known CPU architectures are:

- The x86-family originated by Intel, mostly used in systems that focus on performance, like servers, PCs, and laptops.
- The ARM-family originated by Acorn, mostly used in mobile devices that focus on relatively low power consumption.
- The z/Architecture-family originated by IBM, mostly used in mainframes.

1.2.1 CPU privileges

To protect systems against misuse, the design of a CPU incorporates security features that enable it to guard what code is executed (or not). One of these features is the **CPU Privilege Level (CPL)** system built in the x86-family of CPUs. To explain how this system functions we need to take a detailed look at the inner workings of a CPU.

Every piece of code that is executed by the CPU is assigned a privilege level, ranging from 0 to 3. This privilege level is used to restrict access to three resources: the ability to execute specific instructions in the ALU, access to memory, and access to I/O ports. Code at privilege level 0 has the most privileges, and code at privilege level 3 has the least. Each privilege level is given access to the resources associated with that level *and* the resources available to the lesser levels. In other words, code running at privilege level 3 can only access the resources associated with level 3, whereas code running at privilege level 0 can access all resources. This concept is often depicted as a set of concentric rings with borders between them. When code needs to access data or an instruction in a more privileged ring, it has to use a gate that protects the integrity of the CPU by giving access only to a limited set of resources and only if certain conditions are met.

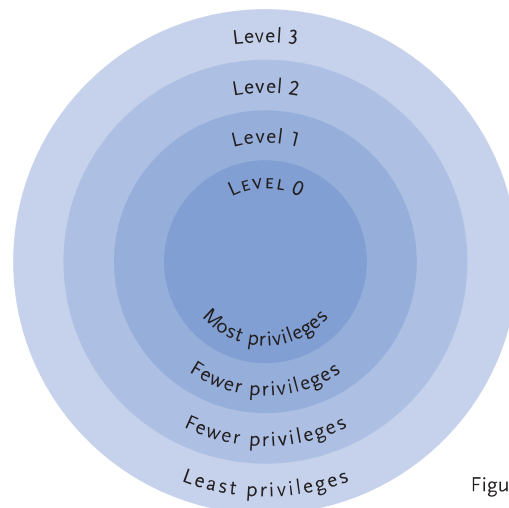


Figure 3: CPU privilege levels

Each memory access request includes a 2-bit Requested Privilege Level (RPL) field. Each memory segment contains a 2-bit field, called the Description Privilege Level (DPL), which defines the minimum privilege level needed to access that memory segment. And the CPU has a code segment register that includes a 2-bit Current Privilege Level (CPL)